VACANCY

About Intrinsic ID

Intrinsic ID is the world's leading digital authentication company, providing the Internet of Things with hardware-based root-of-trust security via unclonable identities for any IoT-connected device. Based on Intrinsic ID's patented SRAM PUF technology, the company's security solutions can be implemented in hardware or software. Intrinsic ID security, which can be deployed at any stage of a product's lifecycle, is used to validate payment systems, secure connectivity, authenticate sensors, and protect sensitive government and military systems. Intrinsic ID technology has been deployed in more than 100 million devices. Award recognition includes the Frost & Sullivan Technology Leadership Award and the EU Innovation Radar Prize. Intrinsic ID security has been proven in millions of devices certified by Common Criteria, EMVCo, Visa and multiple governments. Intrinsic ID's mission: "Authenticate Everything." Visit Intrinsic ID online at www.Intrinsic-ID.com.

Screening policy

All applicants responding to this vacancy are subject to administrative screening. Depending upon the vacancy, the screening may consist of any of the following checks: verification of references, authenticity of identification documents and diploma(s) and receipt of a Certificate of Good Conduct (or equivalent). Further assessment of the

Hardware Design Engineer

Job description
As a hardware design engineer on our team, you will work closely with other hardware and software engineers, to support development of Intrinsic ID's security solutions and products. You will be working on new security-oriented hardware designs and system solutions that are built around the principle of SRAM PUFs

Responsibilities
- Development and implementation of digital hardware modules for FPGA and ASIC (Logic design)
- (automated) verification of hardware modules

Required experience and skills
- Bachelor/Master degree with at least 2 years of relevant professional experience
- Experience with hardware implementations (IP blocks, ASIC, FPGA)
- Professional RTL/VHDL coding skills
- Experience with verification
- Excellent oral and written English

Other appreciated qualities
- Experience with security and cryptography
- Affinity with verification automation
- Experience with Python
- Affinity with embedded software development
- Experience with Verilog

Soft skills
- Good communication skills
- Good organizational skills and excellent problem-solving abilities
- Able to think outside the box: find unexpected ways to solve problems or meet requirements
- Entrepreneurial spirit with an open mind
- Dynamic drive to contribute to expand the company
- Professional disposition: able and willing to work in a team and take responsibility for own tasks